MULTISTAGE AMPLIFYING CIRCUIT

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ABSTRACT
A multistage amplifying circuit includes a first amplifying
circuit that either samples a first analog voltage input or
amplifies a difference between the first analog voltage and a
first digital voltage converted from the first analog voltage, in
response to a control signal. A second amplifying circuit
either samples a second analog voltage input or amplifies a
difference between the second analog voltage and a second
digital voltage converted from the second analog voltage, in
response to the control signal. A common amplifier receives
output voltages of the first amplifying circuit and the second
amplifying circuit and either resets the output voltage of the
first amplifying circuit and determines an output voltage by
using the second amplifying circuit, or resets the output voltage
of the second amplifying circuit and determines an output
voltage by using the first amplifying circuit, in response to the
control signal.